Initial Information Data Sheet

Inventor Information

Inventor One Given Name:

Yasuaki

Family Name:

HIRANO

Name Suffix:

Postal Address Line One

10-42, Naka-machi,

City:

Tenri-shi,

State or Province:

Nara-ken, Japan

Postal or Zip Code:

.

Citizenship Country:

Japan

Correspondence Information

Name Line One:

Barry E. Bretschneider

Name Line Two:

Morrison & Foerster LLP

Address Line One:

2000 Pennsylvania Ave., N.W.

Address Line Two:

City:

Washington

State or Province:

DC

Country:

USA

Postal or Zip Code:

20006-1888 202-887-1500

Telephone:

202-263-8396

Fax: Electronic Mail:

bbretschneider@mofo.com

Application Information

Title Line One:

NONVOLATILE SEMICONDUCTOR MEMORY DEVICE

Title Line Two:

CAPABLE OF PREVENTING OCCURRENCE OF LUTCHUP

Total Drawing Sheets:

10

Formal Drawings?:

Yes

Application Type:

Utility

Docket Number:

204552022100

Representative Information

Representative Customer Number:

25227

Continuity Information

This application is a:

> Application One:

Filing Date:

Prior Foreign Application

Foreign Application One: 2001-054270 Filing Date: February 28, 2001

Country:
Priority Claimed: Japan Yes